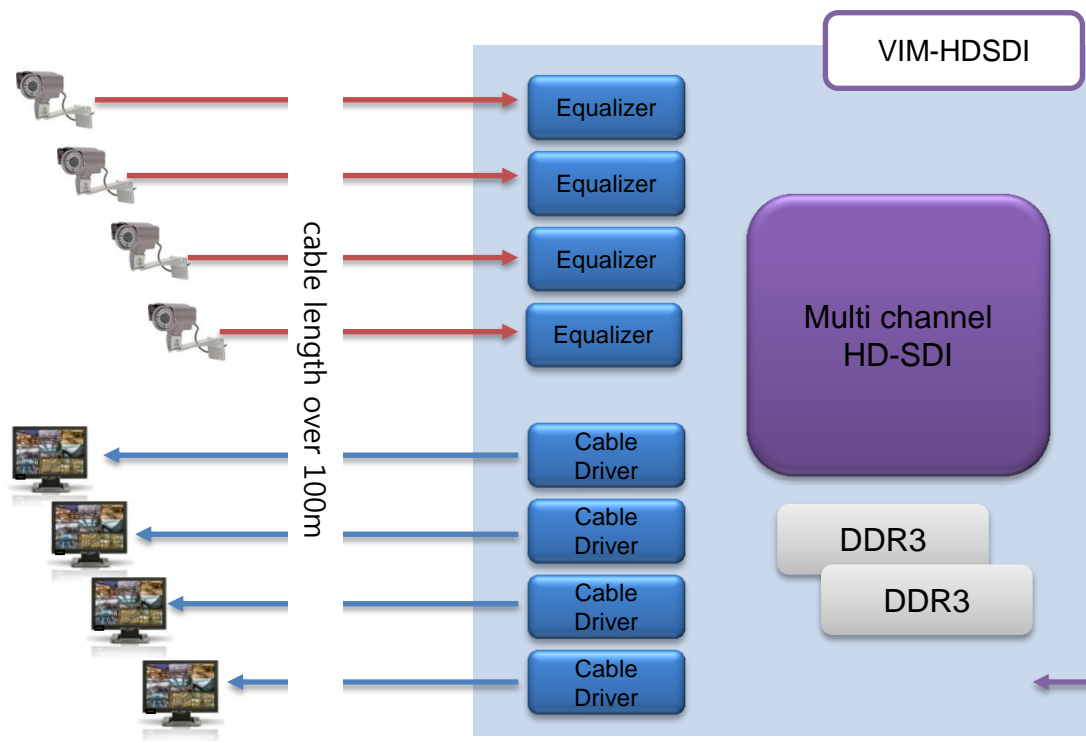


Video Interface Module for TI EVM

TMDXEVM8148 and TMDXEVM368

HD-SDI

CH-Merge
with LVDS



TMDXEVM8148

TMS320DM8148(1GHz Cortex™A8, 750MHz C674x™)
 Dual 32bit DDR3 of 1GB, and 256MB NAND Flash
 Enhanced HD Coprocessor (one HD-VICP2.0)
 1080p60fps Video Stream(H.264 ENC/DEC)
 2D/3D/VG Graphics Acceleration
 I/O Module and 7"LCD Module



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Multi-CH HD-SDI Interface module

Xilinx FPGA : XC6SLX45T or 75T	EVM Connector
Cable Driver and Equalizer : TI	SDI (SD/HD/3G) Interface
DDR-3 : 16bit * 128M * 2EA	4-ch Switch
BNC SDI in/out 4-ch	4-ch Merge (up to 3840x2160)
BNC Ref. clock : 1-ch	Scaler

Application

Video Streaming Media	DVR/Car DVR
Video Communications System	Digital Signage
Machine Vision	Medical Equipment
Multichannel Video Transcoder	ITSC
Video Conferencing	Video Surveillance

Multi-CH HD-SDI Interface Module with TI TMDXEVM8148
VIM-HDSDI : processing 4ch in/out HD video signal transfer with HD-SDI interface

Video Interface Module



Development Environment

Video Interface Module VIM-HSDSI

Xilinx XC6SLX45T or 75T
Cable Driver and Equalizer : TI
DDR-3 : 16bit * 128M * 2EA
BNC SDI in/out 4-ch
BNC Ref. clock : 1-ch
EVM Connector
SDI (SD/HD/3G) Interface
4-ch Switch
4-ch Merge (up to 3840x2160)
Scaler
FPGA I2C Control API
Sample Application

Target EVM TMDXEVM8148

TI DM8148 processor
1GHz CortexA8, 750MHz C674x
Dual 32bit DDR3 of 1GB
256MB NAND Flash
Two USB-OTG ports
Composite and S-video output
1080p HDMI video Output
4-bit SD/MMC interface
Two Gigabit Ethernet port
PCIeX1 lane support
SATA 3Gbps direct Interface
7" 800X480 RGB TFT Display
Ocular 7" capacitive multi-touch panel
Analog SD IN(S-Video, composite video)
Analog HD IN(RGB & VGA)
Digital HD Video capture
Serial camera interface through CSI
12- Bit parallel camera interface

Software Package

Linux EZSDK from TI
Pre-built NFS mountable root file system
Binaries and sources
Device Drivers

JTAG Emulator TMDSEMU560V2STM-UE

Supports TI Processors families:
C28xxx, C5000, C6000, C64x+, C66xx,
ARM, AM1xxx, AM3xxx, OMAP, Davinci
MIPI System Trace with 128 MB trace buffer
up to 100 MHz export clock
USB 2.0 and Ethernet Interface
Supports +1.2V to +4.1V JTAG interfaces

JTAG cable with 4 Target Adapters:
MIPI60-TI14
MIPI60-TI20
MIPI60-TI60
MIPI60-ARM20

Code Composer Studio IDE TMDSCCS-ALLN01 TMDSCCS-ALLF01

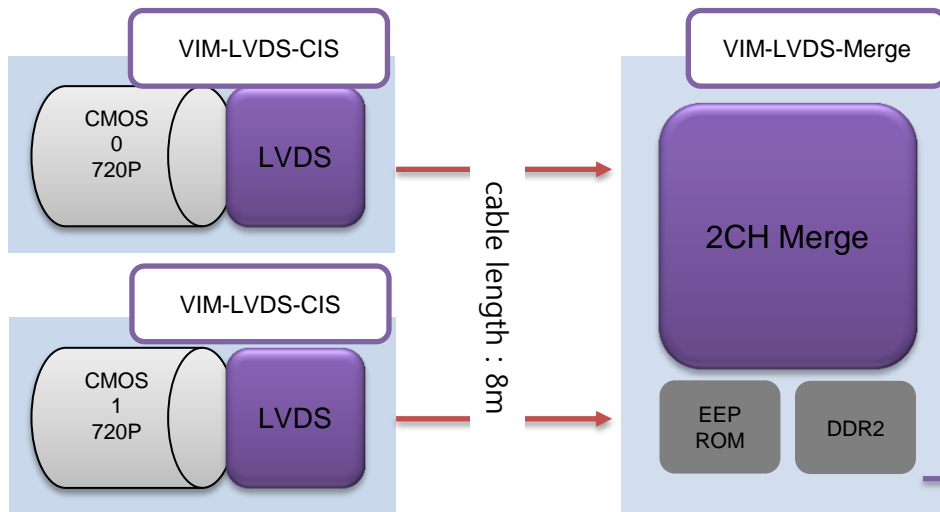
Code Composer Studio IDE
Includes compilers for each of TI's device families, source code editor, project build environment, debugger, profiler, simulators.

Operating System Requirements :
Windows XP and 7, Linux

License Options
Node locked - a license that is tied to a specific computer.
Floating - licenses that can be shared across multiple computers.

견적문의

Tel : 031-781-6900
Fax : 031-781-5400
Email : david@wilcon.co.kr
Web : www.wilcon.co.kr



TMDXEVM368
 TMS320DM368(432MHz ARM926EJ-S)
 128 Mbytes of 400 MHz DDR2, 2 GB NAND Flash
 Two Video Image Co-processors (HDVICP, MJCP)
 720p60fps/1080p30fps video stream(H.264 Enc/Dec)

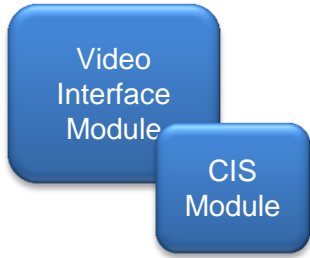


LVDS Interface CIS module
 Xilinx FPGA : XC6SLX4
 720p30fps

2CH Merge & LVDS Interface module
 Xilinx FPGA : XC6SLX9 or LX4
 DDR-2 : 16bit * 128M
 Dual LVDS 720p → 1440p(1280x1440)

Application
 CarBlackBox Video Signage
 Baby Monitor Stereo Vision

2CH Merge & LVDS Interface Module with TI TMDXEVM368
VIM-LVDS-CIS : convert CMOS input to LVDS (1280x720@30fps)
VIM-LVDS-Merge : processing Dual LVDS Video input and channel Merge (1280x1440@30fps)



Development Environment

Video Interface Module VIM-LVDS-Merge

Xilinx XC6SLX9 or LX4
DDR-2 : 16bit * 128M
EVM Connector
2-ch Merge
Dual LVDS 720p30fps
→ 1440p(1280x1440)
Sample Application

CMOS Image Sensor Module VIM-LVDS-CIS

Xilinx XC6SLX4
720p@30fps CMOS Image
Sensor
720p30fps → LVDS

Target EVM TMDXEVM368

DM368 processor
432 MHz ARM926EJ-S
128 MB of 400 MHz DDR2
2 GB NAND Flash
10/100 MBS RMI Ethernet I/F
4.3" color LCD (480 x 272)
Video input port (Composite or S-video)
Component video inputs capture up to
720p60fps/1080p30fps
Composite video DAC output
component video DACs(720p)

Software Package

DVSDK includes TI's HD MPEG-4, H.264,
MPEG2, JPEG codecs
Binaries and sources
Device Drivers

JTAG Emulator TMDSEMU560V2STM-UE

Supports TI Processors families:
C28xxx, C5000, C6000, C64x+, C66xx,
ARM, AM1xxx, AM3xxx, OMAP, Davinci
MIPI System Trace with 128 MB trace buffer
up to 100 MHz export clock
USB 2.0 and Ethernet Interface
Supports +1.2V to +4.1V JTAG interfaces

JTAG cable with 4 Target Adapters:
MIPI60-TI14
MIPI60-CTI20
MIPI60-TI60
MIPI60-ARM20

Code Composer Studio IDE TMDSCCS-ALLN01/F01/F03/F05

Code Composer Studio IDE
Includes compilers for each of TI's device
families, source code editor, project build
environment, debugger, profiler,
simulators.

Operating System Requirements :
Windows XP and 7, Linux

License Options
Node locked - a license that is tied to a
specific computer.
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Development Environment Price Table

Video Interface Module

VIM-HDSDI **\$TBD**

Target EVM

TMDXEVM8148 **\$2,248.99**

TMDXEVM368 **\$1,365.00**

JTAG Emulator

TMDSEMU560V2STM-UE **\$1,495.00**

Code Composer Studio IDE

TMDSCCS-ALLN01 **\$495.00**

TMDSCCS-ALLF01 **\$795.00**

TMDSCCS-ALLF03 **\$1995.00**

TMDSCCS-ALLF05 **\$2994.99**

Video Interface Module

VIM-LVDS-Merge **\$TBD**

CMOS Image Sensor Module

VIM-LVDS-CIS **\$TBD**

Product Engineering Service

Project Summary : TI Processor (DM36x/DM385/DM81xx) 기반 Video Interface 구현 및 Production and Maintenance Service
Initial Fee : TBD

P/N	Initial MOQ	Unit Price	Remark
XC6SLX9	4800	TBD	DSP side
XC6SLX4	4800	TBD	CIS side
XC6SLX45T	TBD	TBD	DSP side
XC6SLX75T	TBD	TBD	DSP side

Including :

1. Prototype build, debug and testing
2. OS porting, Software integration and testing
3. Production and Maintenance support
4. System upgrade : The additional charge will be added.

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